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PATENT DATE

SCANNED 124 Q.A.

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| APPLICATION NO. | CONT/PRIOR | CLASS | SUBCLASS | ART UNIT | EXAMINER |
| 09/977994 | F | 702 | | 2857 | |

APPLICANTS

Makoto Nagata
Atsushi Iwata

TITLE

Method and apparatus for analyzing a source current waveform in a semiconductor integrated circuit

PTO-2040
12/89

ISSUING CLASSIFICATION

| ISSUING CLASSIFICATION | | | | | | | | | | | | |
|------------------------------|--|----------|--|--------------------|-----------------------------------|--|--|--|--|--|--|--|
| ORIGINAL | | | | CROSS REFERENCE(S) | | | | | | | | |
| CLASS | | SUBCLASS | | CLASS | SUBCLASS (ONE SUBCLASS PER BLOCK) | | | | | | | |
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| INTERNATIONAL CLASSIFICATION | | | | | | | | | | | | |
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| <input type="checkbox"/> TERMINAL DISCLAIMER | DRAWINGS | | CLAIMS ALLOWED | |
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims |
| <input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____ | _____ (Assistant Examiner) (Date) | | NOTICE OF ALLOWANCE MAILED | |
| | _____ (Primary Examiner) (Date) | | ISSUE FEE | |
| | | | Amount Due | Date Paid |
| <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed. | _____ (Legal Instruments Examiner) (Date) | | ISSUE BATCH NUMBER | |

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